Two-chip generator shapes synthesizer's sounds

y Jonathan Jacky eattle, Wash.

Jenerating the same adjustable modulating waveforms or a music synthesizer as the circuit proposed by kirschman', but using only two integrated circuits, this generator also works from a single supply. It has, in

addition, separate gate and trigger inputs for providing a nore realistic keyboard response. When gated or triggered, the generator, which is built round Intersil Inc.'s C-MOS 7555 timer, produces a vaveform that passes through four states:

- An exponential attack. An initial decay, or fallback.
- A sustain, or steady dc level.
- A final decay, or release.

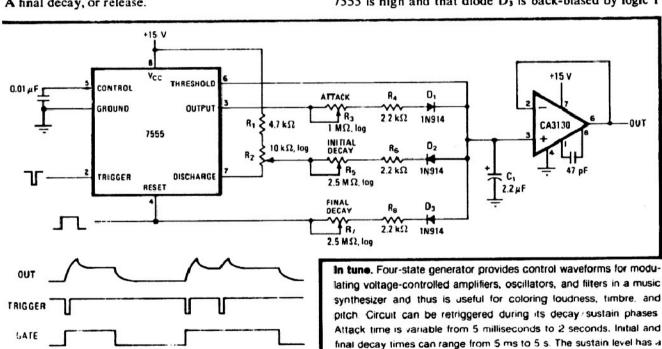
Each of these four parameters is continuously variable,

so that waveforms having a wide variety of shapes can be generated. The waveforms are generated by the sequential charging and discharging of capacitor C1. Here, the 7555

controls the sequencing while diodes switch the currents,

unlike Kirschman's circuit where comparators and flipflops control the stepping and analog switches steer the currents. Furthermore, the 7555 is well suited for handling the two logic signals provided by most synthesizer keyboards—the gate, which is high as long as any key is depressed, and the trigger, which provides a negative pulse as each key is struck. The gate and trigger features eliminate the need to release each key before striking the next to initiate an attack phase.

In the dormant state (the gate input at pin 4 of the 7555 is low), capacitor C₂ is discharged. When the gate goes high and a trigger pulse appears at pin 2, the 7555 output (pin 3) goes high and charges C1 through R3, R4, and D₁, producing the attack segment of the waveform. Note that diode D2 is reverse-biased because pin 7 of the 7555 is high and that diode D₃ is back-biased by logic 1



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signal applied to the gate input.

When the voltage across C1 reaches 10 volts, pin 3 of the 7555 goes low and pin 7 is grounded, terminating the attack phase. D₁ and D₃ are now reverse-biased and C₁

discharges through D2, R5, and R6 to produce the initial decay. The sustain level reached is determined by the voltage divider formed by resistor R1 and potentiometer R₂. During this phase, a second attack can be obtained

by striking another key (see timing diagram). When the last key is released, the gate goes low and C1 will discharge through D₃, R₇, and R₈ to produce the final

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dynamic range of 0 to 10.

decay. The CA3130 operational amplifier serves as a buffer to protect C1 from excessive loading.

 Randall K. Kirschman, "Adjustable et generator colors synthesizer's sounds," Electronics, July 17, 1980, p. 123.