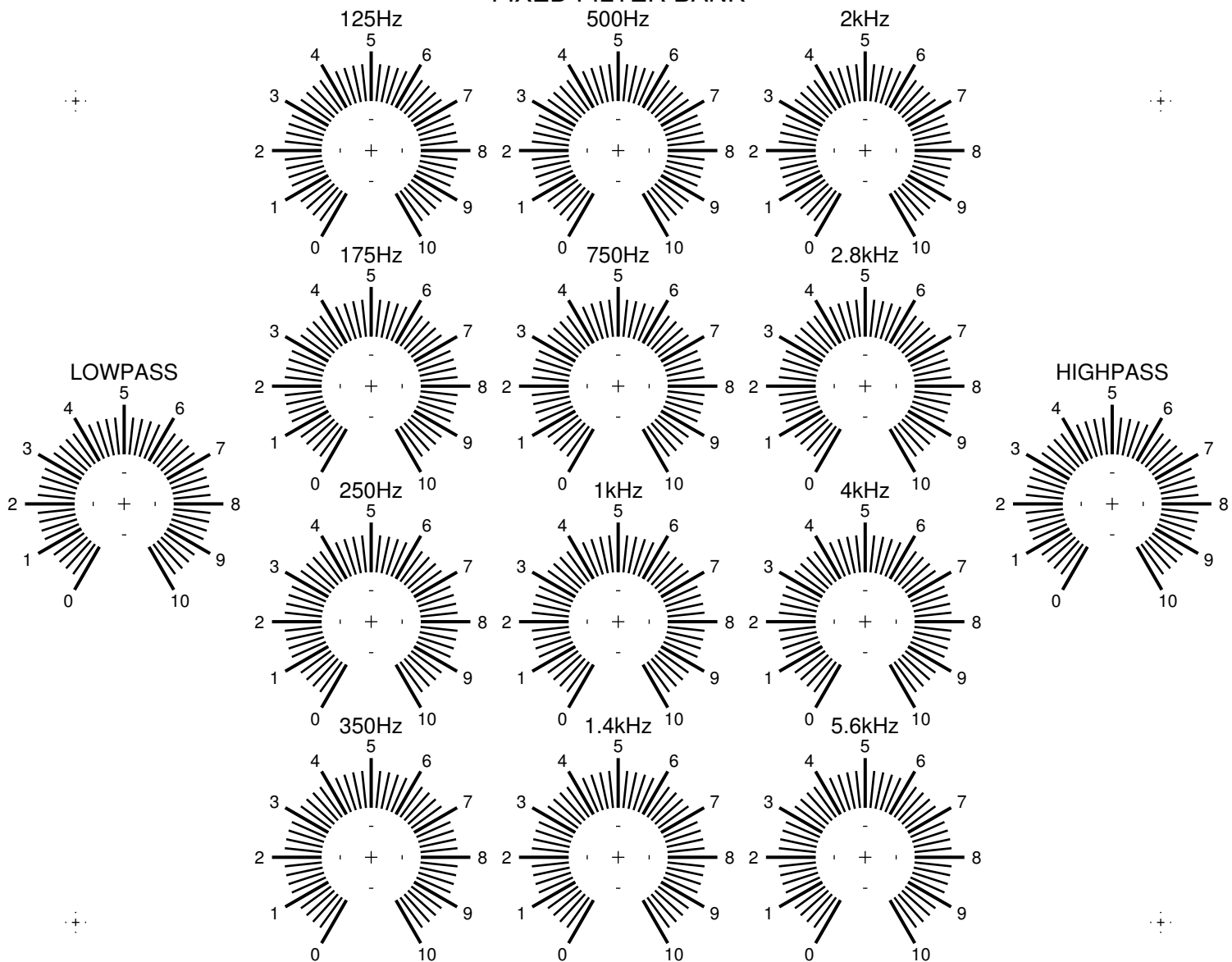


# FIXED FILTER BANK



OUTPUTS

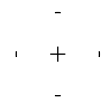
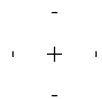
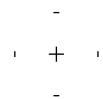
INPUT #1

INPUT #2

ODD CELLS

ALL CELLS

EVEN CELLS



Yusynth